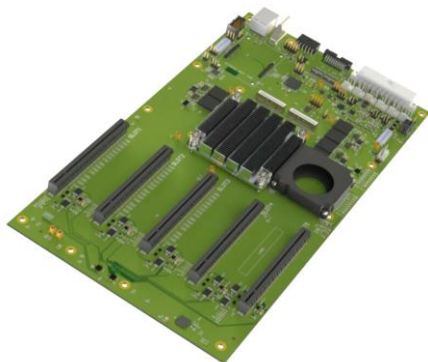


MXB585 PCIe 5.0 Expansion Backplane



Features

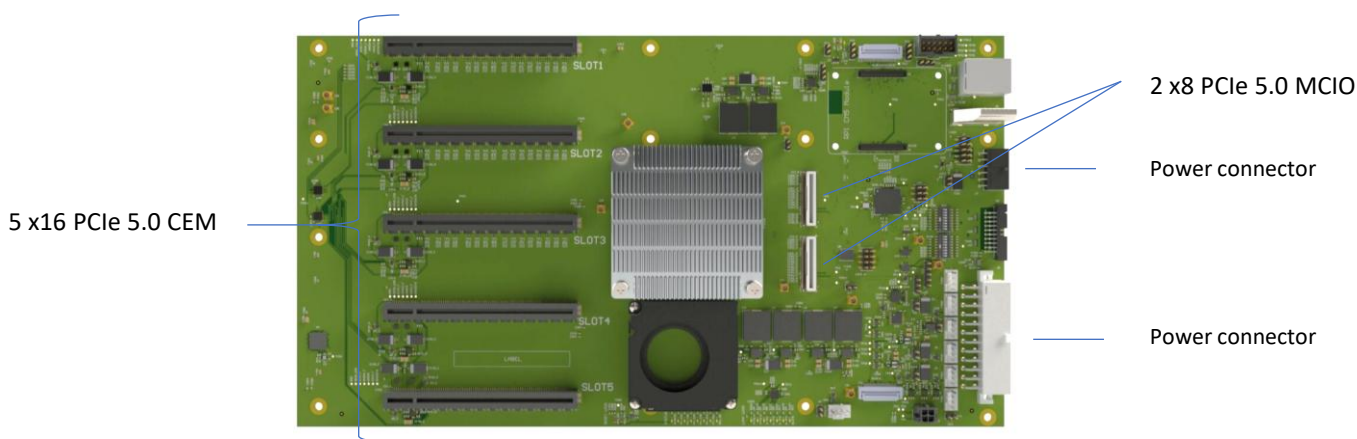
- Microchip Switchtec™ PFX 100xG5
- 5x PCIe 5.0 x16 Dual Width CEM slots
- 2x PCIe 5.0 x8 CopperLink Internal (MCIO)
- Multiconfig for flexible port configurations
- Chassis fan controller
- Inband monitoring and firmware/config updates
- Optional out of band board monitoring and management

Flexible. High-Speed. Built for Demanding Workloads.

The MXB585 is a high-performance PCIe 5.0 expansion backplane designed to deliver ultimate flexibility and scalability. It features five PCIe 5.0 x16 slots that are fully configurable as either upstream or downstream, enabling a broad range of deployment options—from direct host connections to complex daisy-chained systems.

PCIe CopprLink Internal MCIO connectors allow two MXB585 units to be daisy-chained, enabling seamless expansion while preserving bandwidth. These connectors can also be used to extend the MXB585 with an additional PCIe 5.0 x16 CEM slot.

Whether connected remotely via a Dolphin PCIe 5.0 adapter—such as the MXH532 or MXH572—or deployed as a standalone solution using an add-in host card or the CopprLink connection to an SBC, the MXB585 adapts to your system architecture with ease.



Preliminary specifications

Mechanical Dimensions 182 mm x 330.2 mm

PCIe switch Microchip Switchtec® PFX 100xG5 Fanout PCIe® switch with active cooling

slots - 5x PCIe 5.0 x16 Dual Width CEM slots
- 2x PCIe 5.0 x8 MCIO / CopprLink™
Fully configurable for upstream/downstream
Individually overcurrent protected

Link speed PCIe 5.0 - 256 GT/s per port

Power consumption 80W
Compatible with ATX PSU

Operating Environment Operating Temperature: 0°C - 40°C (32°F - 131°F)
Relative Humidity: 5% - 95% (non- condensing)

Storage Environment -40°C to 85°C 5 to 95% non-condensing humidity

PCI Express® Base Specification 5.0
Card Electromechanical Specification 5.0
CopprLink 1.0

Regulatory EMC EU CE
US FCC
Canada ICED
Korea KCC
Taiwan BSMI
Australia RCM

Regulatory Environmental RoHS
China RoHS
REACH
POPs
WFD
WEEE
TSCA Section 8(a)(7)

Management Onboard management controller for SMBUS monitoring and fan control.
Optional CPU for out-of-band board management and configuration/customization over Ethernet with support for RedFish and OpenBMC

SKU and ordering options MXB585
For custom configuration contact Dolphin sales at sales@dolphinics.com or your local representative.