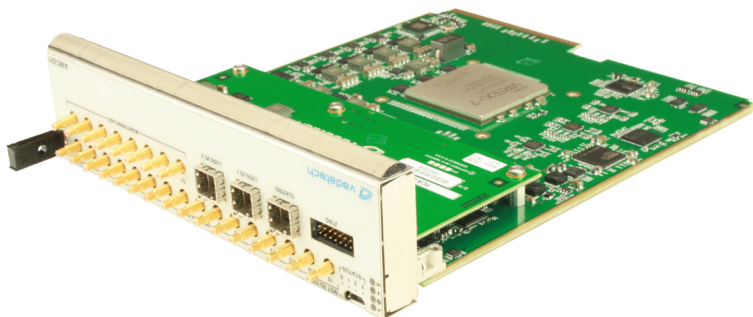


AMC 24 Channels ADC, Mixed Sampling Rate – AMC521

AMC 24 Channels ADC



KEY FEATURES

- Sixteen channel ADC 16-bit @ 250 MSPS
- Eight channel SAR ADC 16-bit @ 650 KSPS simultaneous
- Interface to the FPGA is via JESD204B
- 24 LVDS for Clock/Trig and/or GPIO
- Virtex-7 FPGA 415T or 690T in FFG1158
- Internal/External clock
- Clock Jitter Cleaner with Dual Loop PLLs
- Trig In/Out
- A/D input via SSMC connectors

Benefits of Choosing VadaTech

- Low Jitter Clock distribution via an M-LVDS Cross Bar Switch
- Backplane TCLKA, TCLKB, TCLKC, TCLKD, and FCKLA
- On-board VCXO
- Design utilizes proven VadaTech subcomponents and engineering techniques
- Electrical, mechanical, software, and system-level expertise in house
- Full ecosystem of front and rear boards, enclosures, specialty modules, and test/dev products from one source
- AS9100 and ISO9001 certified company

The AMC521 utilizes eight dual channel JESD204B ADC converters at 250 MSPS with 16-bit resolution for 16 high sampling rate channels. In addition the module has eight 16-bit Successive Approximation Register (SAR)-based ADC at a lower sampling rate for measurements to 650 KSPS. There are also 24 LVDS I/O which can be used for Clock In/Out, Trig In/Out or GPIO.

The AMC521 has an M-LVDS Cross Bar Switch (CBS) for clock distribution which allows clocking to come from the front panel, backplane, or on-board VCXO. The clock outputs to the backplane for distribution to other modules. The AMC521 has a Virtex-7 FPGA with option of 415T or 690T in FFG1158 package.

The AMC ports 4-7 and 8-11 are routed to the FPGA for PCIe, XAUI, SRIO, or other SerDes protocols. AMC ports 0, 1 and 2, 3 are also routed to the FPGA for base channel and storage point-to-point connectivity.

VadaTech can modify this product to meet special customer requirements. Contact us to discuss your application.

AMC 24 Channels ADC, Mixed Sampling Rate – AMC521

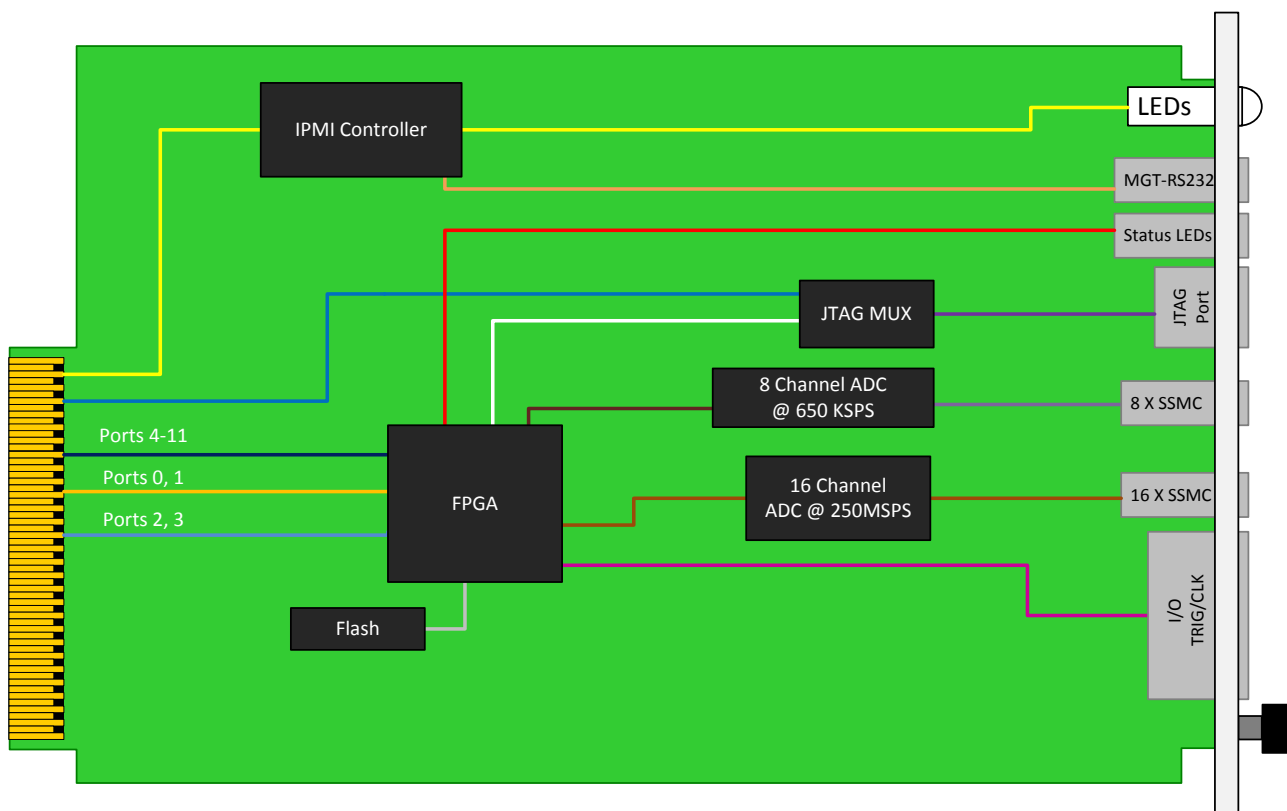
CLOCK JITTER CLEANER

The clock to the ADC has an Ultra-Low RMS Jitter Cleaner with Dual Loop PLLs, with 88 fs RMS jitter (12KHz to 20 MHz) and 162.5 dBc/Hz noise floor at 245.76MHz.

INTEGRATION SERVICES AND APPLICATION-READY PLATFORMS

VadaTech has a full ecosystem of ATCA and μ TCA products including chassis platforms, shelf managers, AMC modules, Switch and Payload Boards, Rear Transition Modules (RTM), Power Modules, and more. The company also offers integration services as well as pre-configured Application-Ready Platforms. Please contact VadaTech Sales for more information.

BLOCK DIAGRAM



FRONT PANEL



SPECIFICATIONS

Architecture		
Physical	Dimensions	Double module, Full-size
		Width 5.85" (148.5 mm)
		Depth 7.11" (180.6 mm)
Type	AMC FPGA	24 ADC (16 fast and 8 slower sampling rate)
Standards		
µTCA	Type	.0, .1, .2 or .3
AMC	Type	AMC.0, AMC.1, AMC.2, AMC.3, AMC.4
Module Management	IPMI	IPMI Version 2.0
PCIe	Lanes	Dual x4 or single x8 via FPGA to AMC
SRIO	Lanes	Dual x4 via FPGA to AMC
Ethernet	Lanes	Dual 10GbE via FPGA and dual GbE via FPGA
Configuration		
Power	AMC521	~25 W, application specific (up to 35 W)
Environmental	Temperature	Operating Temperature: -5° to 55° C (air flow requirements >400 LFM))
		Storage Temperature: -40° to +85° C
	Vibration	1G, 5 to 500 Hz on each axis
	Shock	30Gs each axis
	Relative Humidity	5 to 95 percent, non-condensing
Front Panel	Interface Connectors	FPGA JTAG port
		24 I/O via DensiShield connectors
		ADC inputs via SSMC connectors
		IPMI RS-232
	LEDs	IPMI Management Control
		Four user defined LED
	Mechanical	Hot Swap Ejector Handle
Software Support	Operating Systems	Independent
Other		
MTFB	MIL Hand book 217-F @ TBD Hrs	
Certifications	Designed to meet FCC, CE and UL certifications where applicable	
Standards	VadaTech is certified to both the ISO9001:2000 and AS9100B:2004 standards	
Warranty	Two (2) years	
Trademarks and Disclaimer	The VadaTech logo is a registered trademark of VadaTech, Inc. Other registered trademarks are the property of their respective owners. AdvancedTCA™ and the AdvancedMC™ logo are trademarks of the PCI Industrial Computers Manufacturers Group. All rights reserved. Specification subject to change without notice	

AMC 24 Channels ADC, Mixed Sampling Rate – AMC521

ORDERING OPTIONS

AMC521 – 00C – DEF – GHJ

C = Front Panel

- 1 = Reserved
- 2 = Reserved
- 3 = Full-size
- 4 = Extended-size (8HP)

D = FPGA

- 1 = XC7V415T
- 2 = XC7V690T

E = FPGA, Speed

- 1 = Low
- 2 = High
- 3 = Highest

F = PCIe Option

- 0 = None
- 1 = PCIe on Ports 4-7
- 2 = PCIe on Ports 8-11
- 3 = PCIe on Ports 4-11

G = Front Panel Type

- 0 = MicroTCA.0
- 1 = MicroTCA.1

H = Temperature Range

- 0 = Commercial (-5 to +55° C)
- 1 = Industrial (-20 to +70° C)

J = Conformal Coating

- 0 = None
- 1 = Humiseal 1A33 Polyurethane
- 2 = Humiseal 1B31 Acrylic

RELATED PRODUCTS



AMC520 250 MSPS
DAC Converter



VT891 U4
Chassis



UTC018 1000W
Power Module

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