

# FMC231

FMC Quad ADC 16-bit @ 1 GSPS and  
Dual DAC 16-bit @ 2.8 GSPS

## Key Features

- Quad ADC
  - ADS54J60 16-bit, 1.0 GSPS or ADS54J69 16-bit, 500 MSPS
- Dual DAC
  - DAC39J84 16-bit, 2.8 GSPS
- FPGA Mezzanine Card (FMC) per VITA 57.1
- Excellent dynamic performance
- Front panel interface includes CLK In and Trig In

## Benefits

- High dynamic range for versatility
- Ideal for Radar and Antenna Arrays, Broadband Wireless, Communication Test Equipment, Microwave Receivers, SDR
- Compatible with a broad range of Xilinx- and Altera-based FMC carriers from VadaTech and others
- Electrical, mechanical, software, and system-level expertise in house
- Full system supply from industry leader
- AS9100 and ISO9001 certified company



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# FMC231

The FMC231 is an FPGA Mezzanine Carrier (FMC) per VITA 57 specification. The board has quad ADC (the ADC chips are dual channels) and dual DAC (the DAC chip is dual channel).

The FMC231 utilizes TI ADS54J60 (option for ADS54J69) providing 16-bit conversion rates of up to 1.0 GSPS and a DAC DAC39J84 providing 16-bit conversion rates of up to 2.8 GSPS.

The analog input / output, clock and trigger interface of the FMC231 are routed via SSMC connectors. The internal clock frequency is programmable and the clock is capable of locking to an external reference.

## Block Diagram

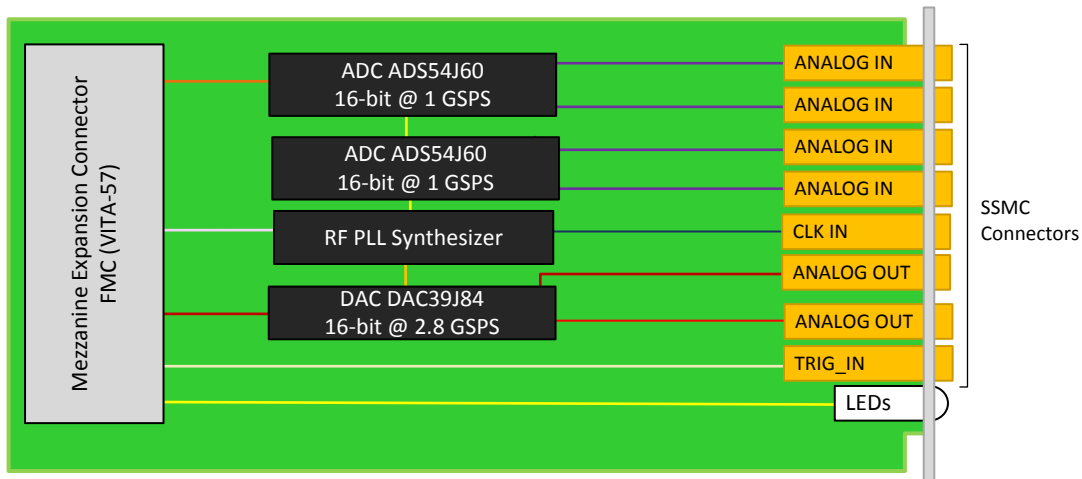


Figure 1: FMC231 Functional Block Diagram

## Front Panel

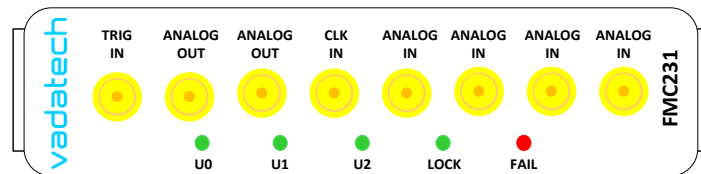


Figure 2: FMC231 Front Panel

# Specifications

<b>Architecture</b>		
<b>Physical</b>	<b>Dimensions</b>	Single module
		Width: 2.71" (69 mm)
		Depth 3.01" (76.5 mm)
<b>Type</b>	<b>FMC</b>	Quad ADCs with dual DAC, Single FMC
<b>Standards</b>		
<b>FMC</b>	<b>VITA-57</b>	ANSI/VITA 57.1-2008
<b>Configuration</b>		
<b>Power</b>	<b>FMC231</b>	~10W
<b>Environmental</b>	<b>Temperature</b>	Operating temperature: -5° to 55° C (air flow requirements >400 LFM)
		Storage Temperature: -40° to +85°C
	<b>Vibration</b>	1G, 5 to 500 Hz on each axis
	<b>Shock</b>	30Gs each axis
	<b>Relative Humidity</b>	5 to 95 per cent, non-condensing
<b>Front Panel</b>	<b>Interface Connectors</b>	8x SSMC
	<b>LEDs</b>	Status
<b>Software Support</b>	<b>Operating System</b>	Agnostic
<b>Conformal Coating</b>		Humiseal 1A33 Polyurethane (Optional)
		Humiseal 1B31 Acrylic (Optional)
<b>Other</b>		
<b>MTBF</b>	MIL Hand book 217-F@ TBD hrs	
<b>Certifications</b>	Designed to meet FCC, CE and UL certifications, where applicable	
<b>Standards</b>	VadaTech is certified to both the ISO9001:2000 and AS9100B:2004 standards	
<b>Warranty</b>	Two (2) years	

## INTEGRATION SERVICES AND APPLICATION-READY PLATFORMS

VadaTech has a full ecosystem of ATCA and  $\mu$ TCA products including chassis platforms, shelf managers, AMC modules, Switch and Payload Boards, Rear Transition Modules (RTM), Power Modules, and more. The company also offers integration services as well as pre-configured Application-Ready Platforms. Please contact VadaTech Sales for more information.

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# Ordering Options

## FMC231 – AB0 – 000 – G0J

A = ADC		G = FMC Board Spacing
0 = ADS54J60 (1 GSPS) 1 = ADS54J69 (500 MSPS)		0 = 10 mm (per VITA-57 specification) 1 = 17.5 mm *
B = DAC (DAC39J84)		
0 = Not installed 1 = Installed		
		J = Conformal Coating
		0 = Commercial (–5° to +55° C), No coating 1 = Commercial (–5° to +55° C), Humiseal 1A33 Polyurethane 2 = Commercial (–5° to +55° C), Humiseal 1B31 Acrylic 3 = Industrial (–20° to +70° C), No coating 4 = Industrial (–20° to +70° C), Humiseal 1A33 Polyurethane 5 = Industrial (–20° to +70° C), Humiseal 1B31 Acrylic 6 = Extended (–40° to +85° C), Humiseal 1A33 Polyurethane ** 7 = Extended (–40° to +85° C), Humiseal 1B31 Acrylic **

\* For use with carriers that require higher mating clearance, such as VadaTech AMC595. Requires full size AMC.

\*\* Conduction cooled, temperature is at edge of module. Consult factory for availability

## Related Products

AMC516



- AMC FPGA carrier for FMC per VITA-57
- Xilinx Virtex-7 690T FPGA in FFG-1761 package with optional P2040
- Supported by DAQ Series™ data acquisition software

AMC532



- AMC FPGA based on Altera Stratix-V (5SGXEA) in F1932 package
- VITA 57.1 FMC HPC Connector (compatible with LPC)
- All FMC LA, HA, HB pairs routed bi-directionally

FMC109



- FPGA Mezzanine Card (FMC) per VITA-57
- Single module
- Quad SPF/SPF+ cages for quad ports

# Contact

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DOC NO. 4FM737-12 REV 01 | VERSION 1.0– AUG/17



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