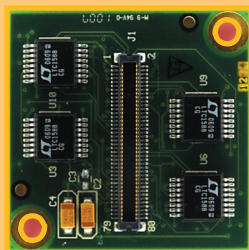
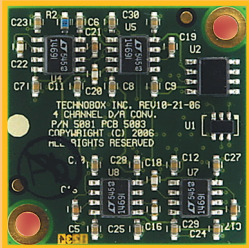
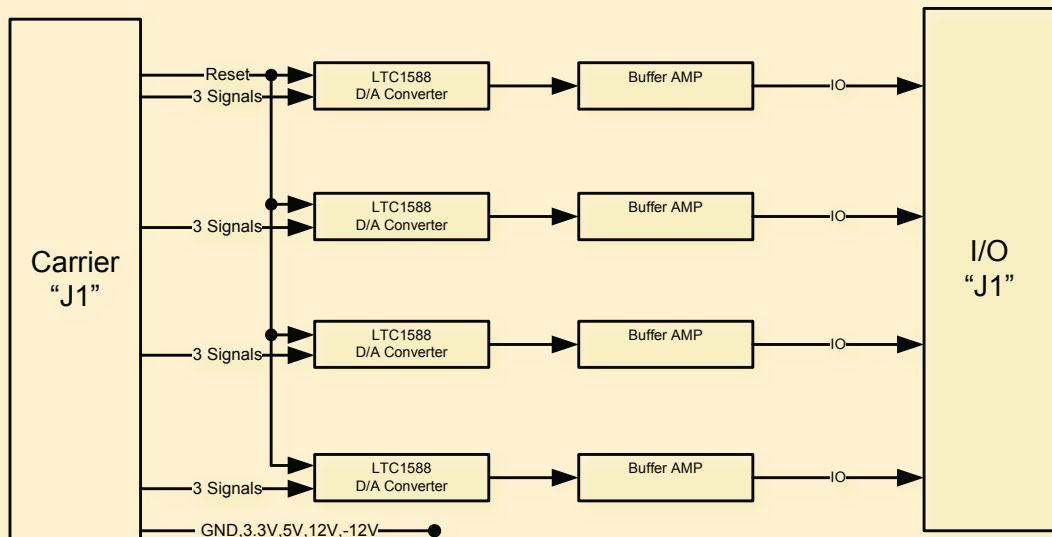


Conversion Module

4-channel, 12-bit Digital to Analog Converter



5081



- Analog output ECM
- Linear Technologies LTC1588 DACs
- 4 channel 12 bit outputs
- 100KHz sample rates
- 1 LSB INL and LSB DNL
- Software selectable output ranges
- 0 to +5V, 0 to +10V, +/-2.5V, +/- 5V, +/-10V -2.5 to +7.5V
- Resets to 0.0 volts on power up
- +/- 10 milliamps output current
- On-board serial identification circuit
- Industrial temperature range
- RoHS compliant
- Patented

Specifications

Temperature (Operating):
-40 to +85 degrees C

Temperature (Storage):
-55 to +100 degrees C

Altitude: Not Specified or
Characterized. Typical similar
equipment is at 15,000 ft.

Humidity (Operating/Storage):
5% to 90% non-condensing.

Vibration: Not specified or
Characterized.

Shock: Not specified or Char-
acterized.

MTBF: Available on request.

Weight: 3 grams.

Power: TBD

Ordering Information

5081: 4-channel, 12-bit Digital
to Analog Converter

Technobox, inc.

140 Mount Holly Bypass
Unit 1
Lumberton, NJ 08048

Tel: 609-267-8988
Fax: 609-261-1011

www.technobox.com

The Technobox P/N 5081 ECM module provides four channels of 12 bit voltage output..

Each channel is software selectable for 0 to +5V, 0 to +10V, +/-2.5V, +/-5V, +/-10V or -2.5 to 7.5V ranges. The outputs reset to 0.0 volts on power up.

The Linear Technologies LTC1588 DACs deliver 1LSB of INL and DNL over the industrial temperature range with up to +/-milliamps of output current. Update rates are 100 Khz per channel.

The P/N 5081 Quad DAC ECM provides 4 voltage outputs. Each output is programmable for 0 to +5V, 0 to +10V, +/-5V, +/-10V, +/-2.5V or -2.5V to +7.5V ranges.

User IO	J1 Pin number	Direction J1	Description
IO0	16	OUTPUT	U9 DAC voltage output
IO1	18	POWER	GND
IO2	28	N/C	No connection
IO3	30	N/C	No connection
IO4	52	OUTPUT	U6 DAC voltage output
IO5	54	POWER	GND
IO6	64	N/C	No connection
IO7	66	N/C	No connection
IO8	65	OUTPUT	U3 DAC voltage output
IO9	63	POWER	GND
IO10	53	N/C	No connection
IO11	51	N/C	No connection
IO12	29	OUTPUT	U10 DAC voltage output
IO13	27	POWER	GND
IO14	17	N/C	No connection
IO15	15	N/C	No connection

Table 1 User IO signal connections

Carrier Data	J1 Pin number	J1 Direction	Description
DA0	10	INPUT	U9, Chip select when Low
DA1	12	INPUT	U9, Clock
DA2	22	INPUT	U9, Serial Data
DA3	24	N/C	No connection
DA4	34	N/C	No connection
DA5	36	N/C	No connection
DA6	46	INPUT	U6, Chip select when Low
DA7	48	INPUT	U6, Clock
DA8	58	INPUT	U6, Serial Data
DA9	60	N/C	No connection
DA10	70	N/C	No connection
DA11	72	N/C	No connection
DA12	71	INPUT	U3, Chip select when Low
DA13	69	INPUT	U3, Clock
DA14	59	INPUT	U3, Serial Data
DA15	57	N/C	No connection
DA16	47	N/C	No connection
DA17	45	N/C	No connection
DA18	35	INPUT	U10, Chip select when Low
DA19	33	INPUT	U10, Clock
DA20	23	INPUT	U10, Serial Data
DA21	21	N/C	No connection
DA22	11	N/C	No connection
DA23	9	N/C	No connection
DA24	40	INPUT	U9,U6,U3 and U10 Clear sets output to 0V when Low
DA25	41	N/C	No connection
DA26	42	N/C	No connection
DA27	39	N/C	No connection

Table 2 Carrier DA signal connections

TOP-ECMA02 5081

